

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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PATENT NO. : 7,089,443 B2

APPLICATION NO.: 10/762,550

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INVENTOR(S) : Albonesi et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 17, line 9, in Claim 1, delete "multiple-clock-domain" and insert - - multiple clock domain - -.

Column 17, line 9, in Claim 1, delete "microprpcessor" and insert - - microprocessor - -.

Column 18, line 25, in Claim 10, delete "one" and insert - - two - -.

Column 18, line 33, in Claim 11, delete "Empty" and insert - - Empty - -.

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